2003/12/29 11:12   2003/12/29 11:12   2003/12/29 11:22   2003/12/29 11:23   2003/12/29 11:24   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29 11:25   2003/12/29	L Number	Hits	Search Text	DB	Time stamp
22	-	432	cycle near stealing	USPAT;	2003/12/29 11:12
2				US-PGPUB;	
000		!			
- 336 controller same memory same latch same code same address (05-PGPUB) (cycle near stealing) and (controller same memory same latch same code same address) (05-PGPUB) (05-PG	-	22		· -	2003/12/23 11:00
Secontroller same memory same latch same   USPAT;   USP			code with address		
Code same address	i ;	326	controller come memory come letch come		2002/12/22 11 05
1	-	336			2003/12/23 11:05
Cycle near stealing) and (controller same memory same latch same code same address)   SePGPUB; EPG; JPO   USPAT;			Code same address		
### ### ### ### ### ### ### ### ### ##	_	1	(cycle near stealing) and (controller same		2003/12/23 11:04
### Says   microcode\$    Says   microcode\$   Says   microcode\$   Controller same memory same latch same code same address) and microcode\$   Controller same memory same latch same code same address same cycle   Says   Sa		_			2003/12/23 11.04
- 8390 microcode\$ - 20 (controller same memory same latch same code same address) and microcode\$ - 76 controller same memory same latch same code same address same cycle - 76 controller same memory same latch same code same address same cycle - 76 controller same memory same latch same code same address same cycle - 80 (half with clock with cycle with same (half with clock with cycle with same (half with clock with cycle with second) - 1006 (half with clock with cycle with second) - 2355 (half with clock with cycle with second) - 2355 ((half with clock with cycle with second)) and (controller with memory with latch second) and (controller with memory with latch with second)) and (controller with memory with latch with signals with word with generats with signals with word with generats with signals with latchs with cycle with clock with cycle with signals with latchs with signals with latchs with signals with latchs with signals with latchs with cycle with clock with cycle with signals with latchs with signals with latchs with signals with latchs with cycle with signals with latchs with cycle with signals with latch with cycle with second)  - 7 (cycle near stealing) and (half with clock with cycle with first) same (half with clock with cycle with second)  - 8 380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and latch scrypt seciphers cryptos)  - 10 380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and latch scrypt seciphers cryptos)) and latch scrypt seciphers cryptos)) and latch scrypt seciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts seciphers cryptos))			and address,		
- 52 (controller same memory same latch same code same address) and microcodes - 76 controller same memory same latch same code same address same cycle - 1073 (half with clock with cycle with second) - 1074 (half with clock with cycle with second) - 1075 (half with clock with cycle with second) - 1076 (half with clock with cycle with second) - 1077 (controller with memory with latch with second)) - 2355 ((half with clock with cycle with second)) - 2355 ((half with clock with cycle with second)) - 3255 ((half with clock with cycle with second)) - 3266 ((half with clock with cycle with second)) - 3275 ((half with clock with cycle with second)) - 3286 ((half with clock with cycle with second)) - 3287 ((half with clock with cycle with second)) - 3287 ((half with clock with cycle with second)) - 3288 ((half with clock with cycle with second)) - 3288 ((controller with memory with latch with address with word with generats with signals with clocks with cycles with address with word with generats with signals with clocks with cycles with signals with clock with cycles with signals with clocks with cycles with signals with clock with cycles with signals with signals with cycles with signals with	-	8390	microcode\$		2003/12/23 11:05
52   (controller same memory same latch same   USPAR;				US-PGPUB;	
Code same address) and microcodes				EPO; JPO	
To controller same memory same latch same code same address same cycle    1073	<del>-</del>	52		USPAT;	2003/12/29 10:10
- 1073 (half with clock with cycle with first) and (half with clock with cycle with second) - 1006 (half with clock with cycle with first) and (half with clock with cycle with second) - 2355 controller with memory with latch uspan; u			code same address) and microcode\$	US-PGPUB;	
Code same address same cycle		!		1	
1013	_	76			2003/12/29 10:50
1073   (half with clock with cycle with first) and (half with clock with cycle with second)   1006   (half with clock with cycle with second)   2355   controller with memory with latch   Same (half with clock with cycle with second)   controller with memory with latch   Same (half with clock with cycle with second)   controller with memory with latch   Same (half with clock with cycle with second)   and (controller with memory with second)   and (controller with memory with latch with second)   and (controller with memory with second)   and (controller with memory with latch with address with word with generat\$ with signal\$ with clock\$ with cycle\$   Same latch\$   Same (controllers with memory \$with latch with address with word with generat\$ with signal\$ with clock\$ with cycle\$   Same latch\$   Same (clock near cycle)   Same latch\$   Same (clock near signal)   Same (address near word)   Same (clock near cycle)   Same latch\$   Same (clock with cycle with second)   Same (clock near signal)   Same (address near word)   Same (clock near cycle)   Same latch\$   Same (clock with cycle with second)   Same (clock with cycle w			code same address same cycle		
and (half with clock with cycle with second) (half with clock with cycle with first) same (half with clock with cycle with first) same (half with clock with cycle with second)  2355 controller with memory with latch  5 ((half with clock with cycle with first) same (half with clock with cycle with second)) and (controller with memory with latch)  9 controller with memory with latch with address with word  1 controller with memory with latch with signals  1 controllers with memory with latch with address with word with generat\$ with signal\$  1 controllers with memory\$ with latch with address with word with generat\$ with signal\$ (control near signal) same (address near word) same (clock near cycle) same latch\$  1 (cycle near stealing) and ((half with clock with cycle with signal\$ (cycle near stealing) and ((half with clock with cycle with second))  3 85 380/261.ccls.  1 380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)  2 236088 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) latch  1 4 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  1 4 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  2 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  3 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  4 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle  5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  6 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  7 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  8 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  9 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$	[ _	1072	(half with alook with analy with firms)		2002/10/00 10 55
Second    1006   (half with clock with cycle with first)   same (half with clock with cycle with   USPAT;   2003/12/29 12:12   12   12   12   12   12   13   13	-	10/3	and (half with clock with cools with	· ·	2003/12/29 10:51
1006   (half with clock with cycle with first)   USPAT;					
Same (half with clock with cycle with second)   US-PCPUB; PO JPO USPAT; US-PGPUB; ENO; JPO USP	_	1006		1	2003/12/29 12:12
Second   Controller with memory with latch		1000			2003/12/23 12:12
- 2355 controller with memory with latch  - 5 ((half with clock with cycle with first) same (half with clock with cycle with second)) and (controller with memory with latch) - 92 controller with memory with latch with dadress with word with generats with signals with clock with cycles - 1 controllers with memorys with latchs with address with word with generats with signals with clock with cycles - 2 (control near signal) same (address near word) same (clock near cycle) same latchs - 7 (cycle near stealing) and ((half with clock with cycle with first) same (half with clock with cycle with second)) - 85 380/261.ccls 508304 encods decods encrypts decrypts enciphers cryptos) - 1 380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) latch - 236088 (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and latch - 14 (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle and clock (380/261.ccls. and (encods decods encrypts decrypts enciphers cryptos)) and cycle					
Solution	-	2355			2003/12/29 10:53
S			<u>-</u>	1	
Same (half with clock with cycle with second) and (controller with memory with latch)   1				EPO; JPO	
second) and (controller with memory with latch)   latch)   controller with memory with latch with address with word   USPAT; US-PGPUB; EPO; JPO   USPAT; US-PGPUB; signal\$   controller\$ with memory \$with latch with address with word with generat\$ with signal\$ with clock\$ with generat\$ with signal\$ with clock\$ with cycle\$   (control near signal) same (address near word) same (clock near cycle) same latch\$   USPAT; US-PGPUB; EPO; JPO   USPAT; USPAT	-	5		USPAT;	2003/12/29 10:53
latch    controller with memory with latch with   ddress with word   USPAT;   USPA				•	
- 92 controller with memory with latch with address with word  - 41 controller with memory with latch with address with word with generat\$ with signal\$  - 1 controllers with memory\$ with latch\$ with address with word with generat\$ with signal\$ with clock\$ with cycle\$  - 2 (control near signal) same (address near word) same (clock near cycle) same latch\$  - 3 (cycle near stealing) and ((half with clock with cycle with first) same (half with clock with cycle with second))  - 4 508304 encod\$ decod\$ encrypt\$ decrypt\$ encipher\$  - 508304 encod\$ decod\$ encrypt\$ decrypt\$ encipher\$  - 7 crypto\$  - 7 (sycle near stealing) and (chalf with clock with cycle with first) same (half with clock with cycle with second))  - 85 380/261.ccls.  - 7 (sycle near stealing) and (chalf with clock with cycle with second))  - 85 380/261.ccls.  - 85 380/261.ccls.  - 10 380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)  - 236088 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) latch  - 3 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and latch  - 4 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 6 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 7 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 7 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 8 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 8 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)) and cycle and clock  - 8 (380/261.ccls. and (encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$)				EPO; JPO	
address with word   US-PGPUB;   EPO; JPO   USPAT;   US-PGPUB		00	,		0000/10/00 10 50
- 41 controller with memory with latch with address with word with generat\$ with uspant; signal\$	-	92			2003/12/29 10:58
- 41 controller with memory with latch with address with word with generat\$ with signal\$ (SPAT; US-PGPUB; EPG; JPO address\$ with word\$ with generat\$ with signal\$ with clock\$ with cycle\$ (Control near signal) same (address near word) same (clock near cycle) same latch\$ (SPAT; US-PGPUB; EPG; JPO word) same (clock near cycle) same latch\$ (SPAT; US-PGPUB; EPG; JPO word) same (clock near cycle) same latch\$ (SPAT; US-PGPUB; EPG; JPO word) same (clock near cycle) same (half with clock with cycle with first) same (half with clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second)) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) (SPAT; US-PGPUB; EPG; JPO word) same/clock with cycle with second) with clock with cycle with second) word) same/clock with cycle with second) word) same/clock with cycle with cycle with second) word) same/clock wi			address with word		
address with word with generat\$ with signal\$ controller\$ with memory\$ with latch\$ with usPAT;	_	41	controller with memory with latch with		2003/12/29 10.59
Signals   Controller\$ with memory\$ with latch\$ with address\$ with word\$ with generat\$ with signal\$ with clock\$ with cycle\$   2 (control near signal) same (address near word) same (clock near cycle) same latch\$   USPAT; USPAT; USPAT; Clock with cycle with first) same (half with clock with cycle with second)   USPAT; USPAT		3.			2003/12/29 10.38
-   1   controller\$ with memory\$ with latch\$ with address\$ with word\$ with generat\$ with signal\$ with clock\$ with cycle\$   Control near signal) same (address near word) same (clock near cycle) same latch\$   USPAT; USPGUB; EPG; JPO   USPAT; USPAT; USPAT; USPGUB; EPG; JPO   USPAT; USPAT; USPAT; USPGUB; EPG; JPO   USPAT;				1	
address\$ with word\$ with generat\$ with signal\$ with clock\$ with cycle\$ EPO; JPO USPAT; US-PGPUB; USPAT; U	-	1			2003/12/29 11:06
2   (control near signal) same (address near word) same (clock near cycle) same latch\$   USPAT; US-PGPUB; EPO; JPO   USPAT; US-PGPUB; EPO; J				US-PGPUB;	
word) same (clock near cycle) same latch\$   US-PGPUB;   EPO; JPO   Clock with cycle with first) same (half   US-PGPUB;   EPO; JPO   USPAT;   USPAT;   US-PGPUB;			signal\$ with clock\$ with cycle\$	EPO; JPO	
To (cycle near stealing) and ((half with clock with cycle with first) same (half with clock with cycle with first) same (half with clock with cycle with second))  85 380/261.ccls.  10	-	2	(control near signal) same (address near		2003/12/29 11:09
7 (cycle near stealing) and ((half with clock with cycle with first) same (half with clock with cycle with second))  85 380/261.ccls.  10 2003/12/29 11:20  10 2003/12/29 11:20  10 2003/12/29 11:20  11 20 2003/12/29 11:20  11 20 2003/12/29 11:20  11 20 2003/12/29 11:20  12 2003/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:22  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:23  20 203/12/29 11:25  20 203/12/29 11:25  20 203/12/29 11:25  20 203/12/29 11:25  20 203/12/29 11:25  20 203/12/29 11:25  20 203/12/29 11:25  20 203/12/29 11:25			word) same (clock near cycle) same latch\$		
clock with cycle with first) same (half with clock with cycle with second)   EPO; JPO   USPAT; US-PGPUB; EPO; JP		_		ł	0000/10/00
### ### ##############################	-	7		1	2003/12/29 11:20
- 85 380/261.ccls. USPAT; US-PGPUB; EFO; JPO USPAT; US-PGPUB;					
Solid	_	0 %			2003/12/20 11:20
- 508304 encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ crypto\$  71 380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EFO; JPO USPAT; US-PGPUB; EFO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB;			000/201.0013.		2003/12/29 11:32
- 508304 encod\$ decod\$ encrypt\$ decrypt\$ encipher\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; US-PGPUB; EPO; JPO USPAT; US-PGPUB; US-P					
Crypto\$   US-PGPUB; EPO; JPO   JPO   JPO   JPO   USPAT;   US-PGPUB; EPO; JPO   USPAT;   US-PGPUB;   EPO; JPO   USPAT;   US-PGPUB;   EPO; JPO   USPAT;   US-PGPUB;		508304	encod\$ decod\$ encrypt\$ decrypt\$ encipher\$	The state of the s	2003/12/29 11:22
To a solution of the contract					
- 71 380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; Clock USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO USP			_ <del></del>		
decrypt\$ encipher\$ crypto\$)  236088 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB;	-	71	380/261.ccls. and (encod\$ decod\$ encrypt\$		2003/12/29 11:22
- 236088 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; J			decrypt\$ encipher\$ crypto\$)		
decrypt\$ encipher\$ crypto\$)) latch  (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; decrypt\$ encipher\$ crypto\$)) and cycle and cycle and clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; Clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; DSPAT; US-PGPUB;				EPO; JPO	
- 3 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; Clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; Clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; Clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; US-PGPUB; Clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; US-PGP	-	236088			2003/12/29 11:23
3 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; decrypt\$ encipher\$ crypto\$)) and latch  - 14 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; decrypt\$ encipher\$ crypto\$)) and cycle and cycle and clock - 5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; decrypt\$ encipher\$ crypto\$)) and (cycle USPAT; US-PGPUB;			decrypt\$ encipher\$ crypto\$)) latch		
decrypt\$ encipher\$ crypto\$)) and latch  US-PGPUB; EPO; JPO USPAT; US-PGPUB; EPO; JPO USPAT; US-PGPUB; Clock	_		/200/261 colo and /a====40 d====40		0000/10/00 11 1:
- 14 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; 2003/12/29 11:25 decrypt\$ encipher\$ crypto\$)) and cycle and clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; US-PGPUB; EPO; JPO USPAT; decrypt\$ encipher\$ crypto\$)) and (cycle USPAT; US-PGPUB;	-	3	(300/201.CCIS. and (encods decods encrypts		2003/12/29 11:24
- 14 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; decrypt\$ encipher\$ crypto\$)) and cycle and clock (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; EPO; JPO USPAT; decrypt\$ encipher\$ crypto\$)) and (cycle USPAT; US-PGPUB;					
decrypt\$ encipher\$ crypto\$)) and cycle and US-PGPUB; clock  5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; 2003/12/29 11:25 decrypt\$ encipher\$ crypto\$)) and (cycle US-PGPUB;	_	14	(380/261.ccls. and (encods decods onor		2003/12/20 11.25
clock - (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; 2003/12/29 11:25 decrypt\$ encipher\$ crypto\$)) and (cycle US-PGPUB;			decrypt\$ encipher\$ crypto\$)   and cycle and		2003/12/29 11:25
5 (380/261.ccls. and (encod\$ decod\$ encrypt\$ USPAT; decrypt\$ encipher\$ crypto\$)) and (cycle US-PGPUB;					
decrypt\$ encipher\$ crypto\$)) and (cycle US-PGPUB;	-	5			2003/12/29 11.25
			decrypt\$ encipher\$ crypto\$)) and (cycle		
			near clock)	EPO; JPO	

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_	3	380.clas. and (cycle near stealing)	USPAT; US-PGPUB;	2003/12/29 11:28
_	589	380/46.ccls.	EPO; JPO USPAT; US-PGPUB;	2003/12/29 11:32
_	106	380/274.ccls.	EPO; JPO USPAT; US-PGPUB;	2003/12/29 12:09
_	4944	address with code with word	EPO; JPO USPAT; US-PGPUB;	2003/12/29 12:11
_	611	(first with half with (clock near cycle)) same (second with half with (clock near cycle))	EPO; JPO USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:28
-	10	(address with code with word) and ((first with half with (clock near cycle)) same (second with half with (clock near cycle)))	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:32
_	4856	(first with (clock near cycle)) same (second with (clock near cycle))	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:47
_	73	(address with code with word) and ((first with (clock near cycle)) same (second with (clock near cycle)))	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:49
-	64576	signal with control with address	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:51
_	1434	(signal with control with address) and ((first with (clock near cycle)) same (second with (clock near cycle)))	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:51
-	21850	signal with control with address with generat\$	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:53
_	613	<pre>(signal with control with address with generat\$) and ((first with (clock near cycle)) same (second with (clock near cycle)))</pre>	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:56
_	582	_ · · · ·	USPAT; US-PGPUB; EPO; JPO	2003/12/29 12:53
-	3128		USPAT; US-PGPUB; EPO; JPO	2003/12/29 16:00
_	71	(signal with control with address with generat\$ with memory with stor\$) and ((first with (clock near cycle)) same (second with (clock near cycle)))	USPAT; US-PGPUB; EPO; JPO	2003/12/29 15:08
-	11		USPAT; US-PGPUB; EPO; JPO	2003/12/29 15:12
-	83	(signal with control with address with generat\$ with memory with stor\$ with decod\$) and ((first with cycle) same	USPAT; US-PGPUB; EPO; JPO	2003/12/29 15:23
-	1	memory same (address with stor\$ with decod\$) and ((first with cycle) same (second with cycle)) and (cycle near	USPAT; US-PGPUB; EPO; JPO	2003/12/29 15:28
-	1	stealing) (signal with control with generat\$) same memory same (address with stor\$ with decod\$) and ((first with cycle) same (second with cycle)) and (cycle near	USPAT; US-PGPUB; EPO; JPO	2003/12/29 15:28
-	6	steal\$) (signal with control with generat\$) same memory same (address with stor\$ with decod\$) and (cycle near steal\$)	USPAT; US-PGPUB; EPO; JPO	2003/12/29 15:34

	39	(address with decod\$) and (cycle near	HCDAM.	1 0000 /10 /00 10 11
-	39	steal\$) same clock with signal	USPAT;	2003/12/30 10:11
	1	Steals) Same Clock with Signal	US-PGPUB;	
	_	( ) ) ( )	EPO; JPO	
-	7	(address with decod\$) same (cycle near	USPAT;	2003/12/29 15:40
		steal\$) same clock with signal	US-PGPUB;	
			EPO; JPO	
-	6	(cycle near steal\$) same (clock with	USPAT;	2003/12/29 15:41
1		signal\$ with decod\$ with address\$)	US-PGPUB;	
	1		EPO; JPO	
-	375	365/238.5.ccls.	USPAT	2003/12/29 15:59
_	13	365/238.5.ccls. and signal with control	USPAT;	2003/12/29 16:00
		with address with generat\$ with memory	US-PGPUB;	2000,12,23 10.00
		with stor\$	EPO; JPO	
_	3906	accelerat\$ with generat\$ with signal\$ with	USPAT;	2003/12/30 10:20
	3300	control	US-PGPUB;	2003/12/30 10:20
		Control	EPO; JPO	
	836	augleé poor stoolé		2002/10/20 10 12
-	030	cycle\$ near steal\$	USPAT;	2003/12/30 10:13
			US-PGPUB;	l
	_		EPO; JPO	
-	1	(accelerat\$ with generat\$ with signal\$	USPAT;	2003/12/30 10:13
		with control) and (cycle\$ near steal\$)	US-PGPUB;	
			EPO; JPO	
-	33589	clock with generat\$ with signal\$ with	USPAT;	2003/12/30 10:28
1		control	US-PGPUB;	<u> </u>
			EPO; JPO	
-	114	(cycle\$ near steal\$) and (clock with	USPAT;	2003/12/30 10:21
1		generat\$ with signal\$ with control)	US-PGPUB;	
			EPO; JPO	
-	2081	clock with generat\$ with signal\$ with	USPAT;	2003/12/30 10:52
		control and pipelin\$	US-PGPUB;	2000, 12,00 10.02
}		concide and pipewin,	EPO; JPO	
_	42	(clock with generat\$ with signal\$ with	USPAT;	2003/12/30 10:53
	12	control and pipelin\$) and (cycle\$ near	US-PGPUB;	2003/12/30 10.33
1		steal\$)	EPO; JPO	
ļ_	181	address\$ with cod\$ with word with clock	USPAT;	2003/12/30 10:53
	101	address with cods with word with clock		2003/12/30 10:53
			US-PGPUB;	
		/-31 115	EPO; JPO	0000/10/00 15 55
-	5	\	USPAT;	2003/12/30 11:09
		control and pipelin\$) and (address\$ with	US-PGPUB;	
		cod\$ with word with clock)	EPO; JPO	
-	2041	365/189.05.ccls.	USPAT	2003/12/30 11:09
-	89	(clock with generat\$ with signal\$ with	USPAT;	2003/12/30 11:10
		control and pipelin\$) and 365/189.05.ccls.	US-PGPUB;	
			EPO; JPO	1